

OTHER PUBLICATIONS

IEEE Electron Device Letters, vol. 10, No. 10, pp. 520-522, entitled "Power Semiconductor Device Figure of Merit for High-Frequency Application," Dated Oct., 1989, Authored

by B. Jayant Baliga.

IEEE Electron Device Letters, vol. 11, No. 11, pp. 455-457 entitled "Potential Impact of Emerging Semiconductor Technologies on Advanced Power Electronic Systems," Dated Nov., 1990, Authored by Krishna Shenai.